

6/7/04

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

**Title of
Invention****ECHO CANCELLATION DEVICE FOR FULL DUPLEX
COMMUNICATION SYSTEMS**

Application Number :

Confirmation Number:

First Named Applicant: Chen-Chih Huang

Attorney Docket Number: REAP0023USA

Art Unit:

Examiner:

Search string: (6373908).pn

US Patent Documents**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
R2	1	6373908	2002-04-16	Chan			

Signature

Examiner Name	Date
Ram Smith	3/28/07

8/10/05

PTO/SB/08A (08-03)

Approved for use through 07/31/2008. OMB 0651-0031

U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	1
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Complete If Known

Application Number	10/709,935
Filing Date	06/07/2004
First Named Inventor	Chen-Chih Huang
Art Unit	2663
Examiner Name	NGUYEN, CHAU T
Attorney Docket Number	REAP0023USA

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Examiner Signature	Ram S. V.	Date Considered	3/30/17
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PTQ/SB/08A (08-03)

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Examiner Name	NGUYEN, CHAU T
Attorney Docket Number	REAP0023USA

Sheet	1	of	2
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ² Number * Kind Code ³ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
Rg	2	TW 318989 (chimer)	11-01-1997	No. Isomulation		+

Examiner
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Sam Smith

Date	
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		Application Number	10/709,935
		Filing Date	06/07/2004
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		Art Unit	2663
		Examiner Name	NGUYEN, CHAU T
Sheet 2 of 2	Attorney Docket Number	REAP0023USA	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RS	3	"15MHz, BIMOS Operational Amplifier with MOSFET Input/CMOS Output.", HARRIS Semiconductor, September 1998, pp. 1, 4, CA3130, CA3130A, No. 817.4., Harris Corporation 1998.	

Examiner Signature	Ram Singh	Date Considered	3/28/07
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